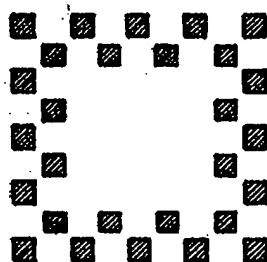


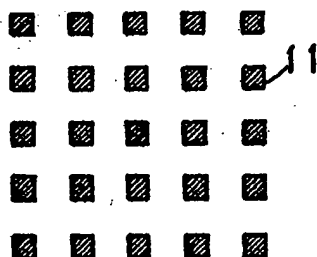
PRIOR ART

*Fig. 1.*  
PERIMETER I/O

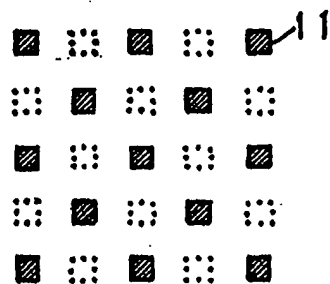


PRIOR ART

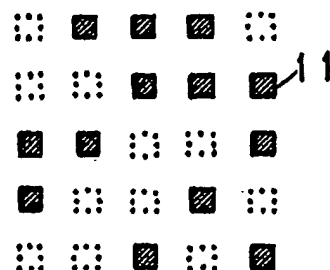
*Fig. 2.*  
STAGGERED PERIMETER I/O



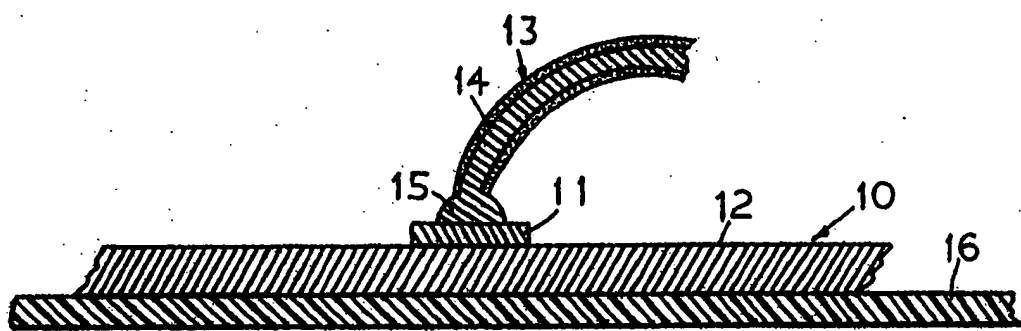
*Fig. 3.*  
FULL ARRAY



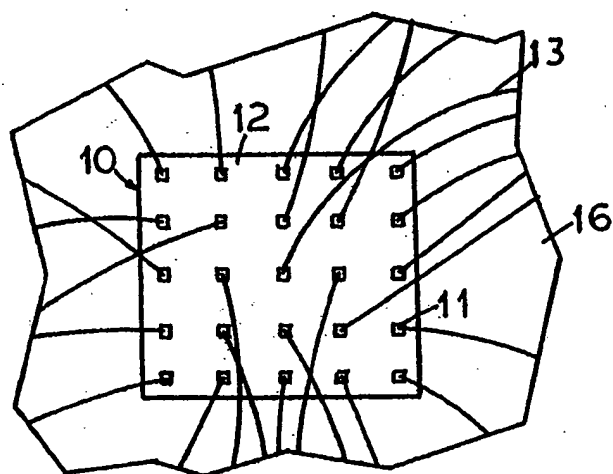
*Fig. 4.*  
DEPOPULATED ARRAY



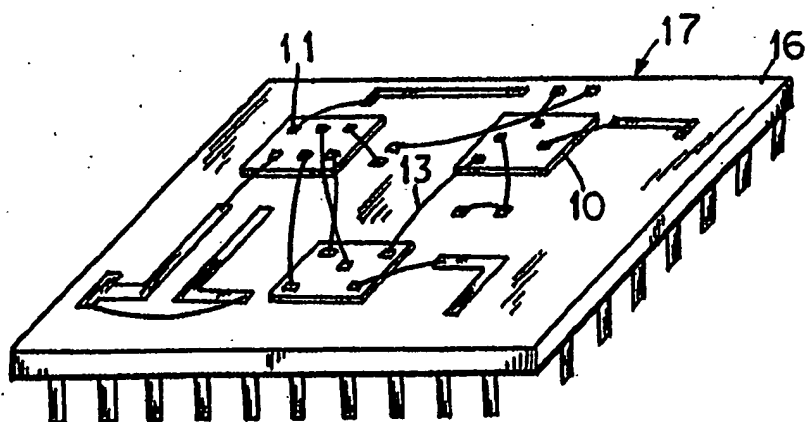
*Fig. 5.*  
RANDOM ARRAY



*Fig. 6.*



*Fig. 7.*



*Fig. 8.*

BEST AVAILABLE COPY

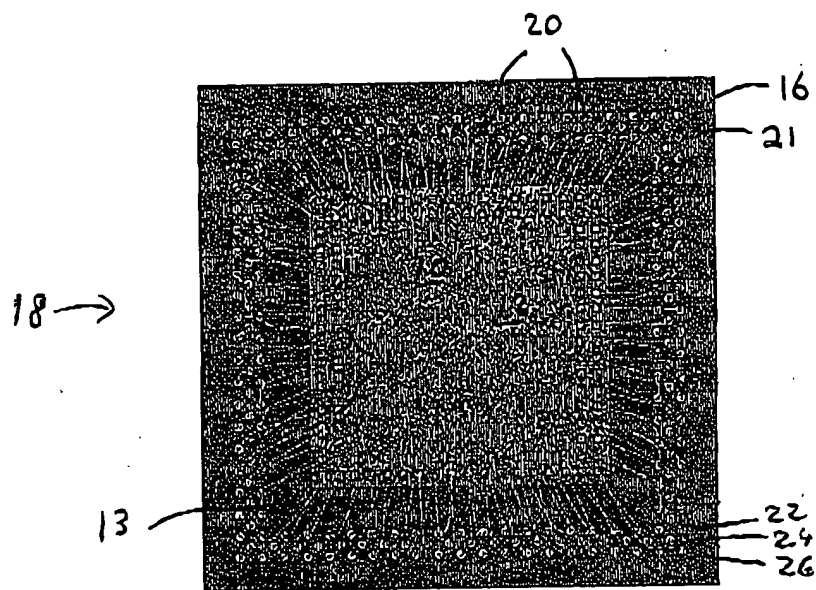


Fig. 9

BEST AVAILABLE COPY

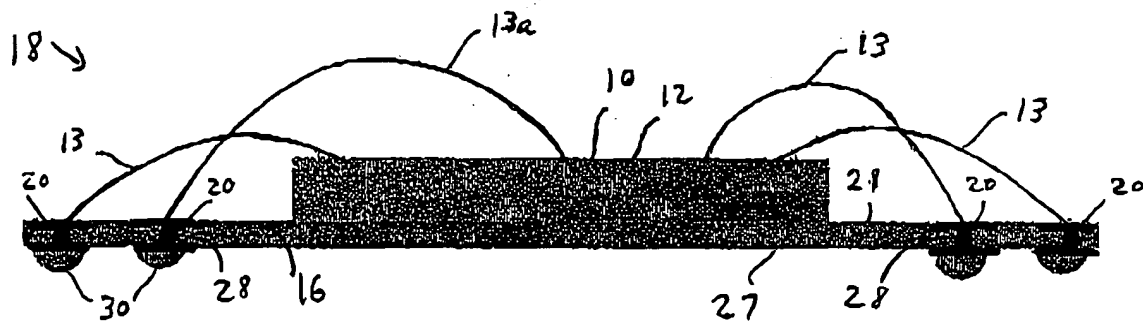


Fig. 10

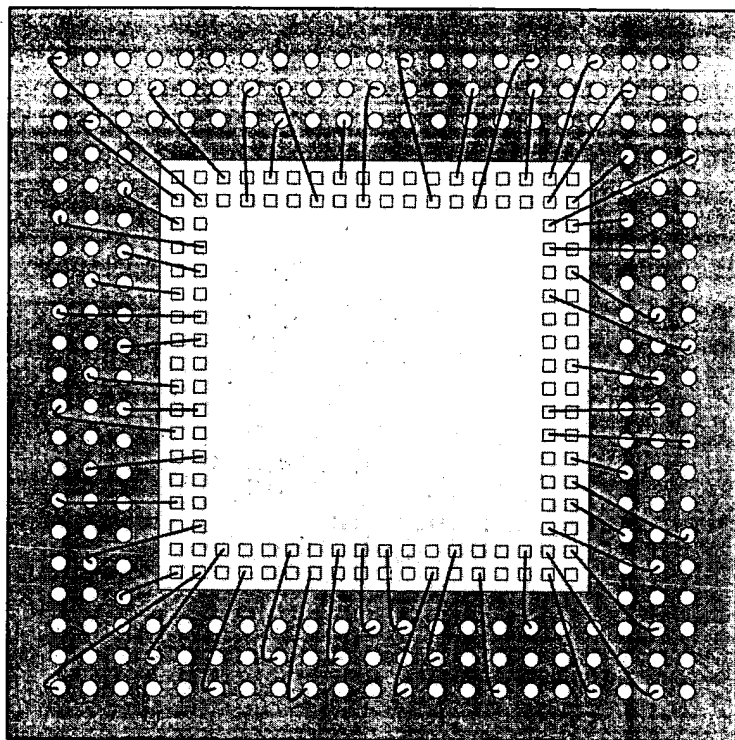


Fig. 11b

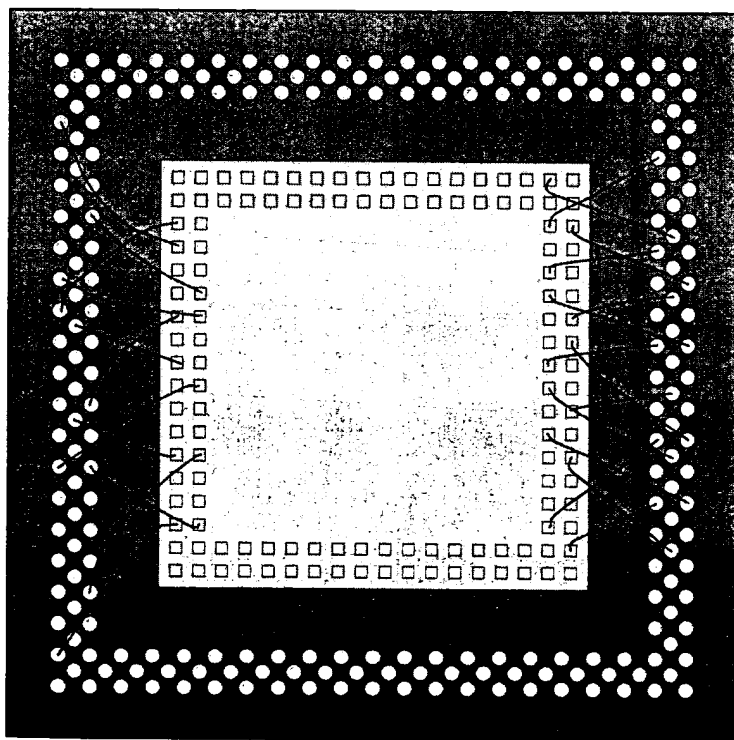


Fig 11a

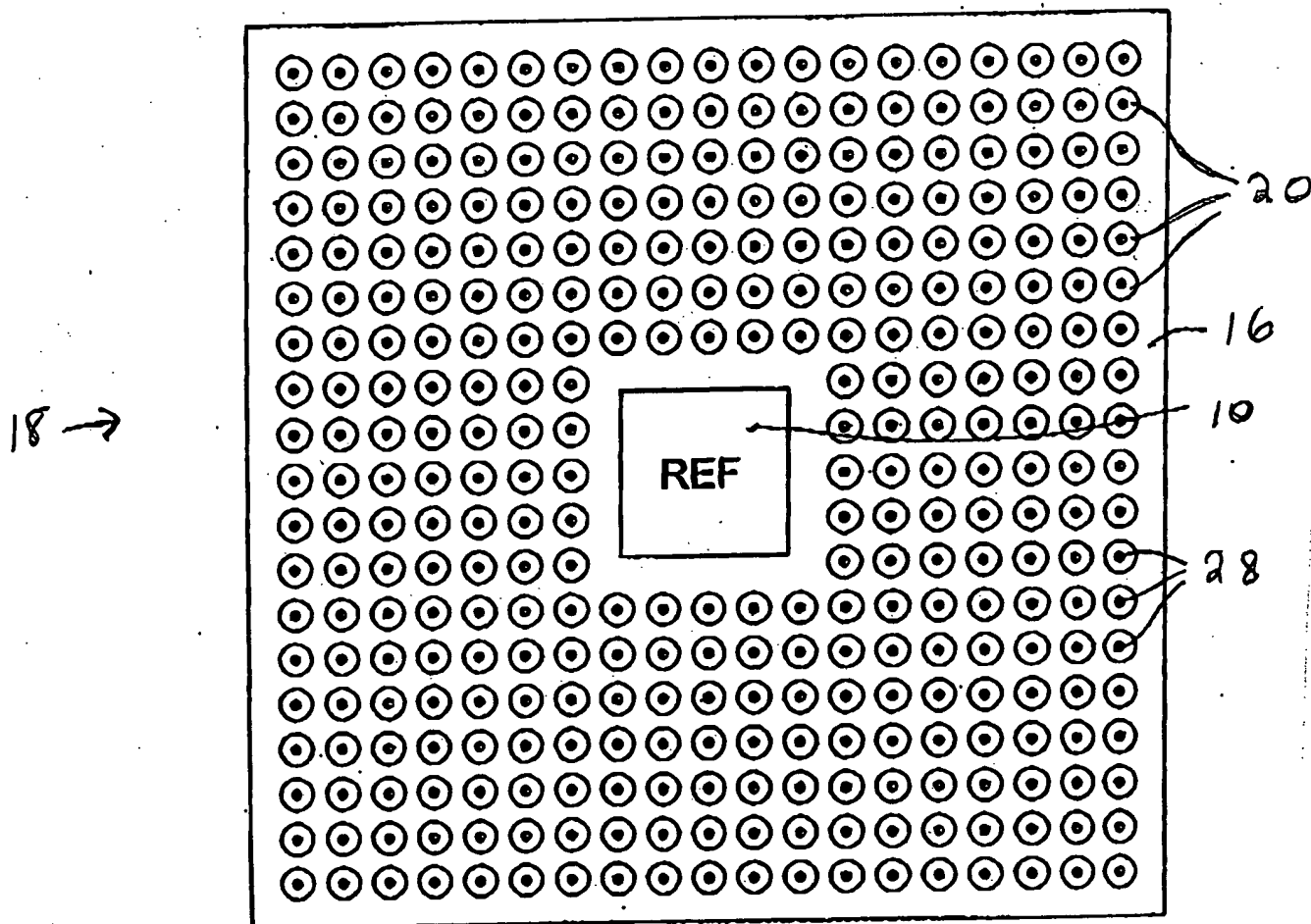


Fig. 12